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THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

WILLIAM R. ROCHE  
DAVID DONGGANG WU  
MASSUD AMINPUR  
SCOTT D. LUNING

§  
§  
§ Examiner: Khanh B. Duong  
§  
§ Group Art Unit: 2822  
§  
§ Att'y Docket: 2000.032200/TT3634  
§

Serial No.: 09/676,197

Filed: September 28, 2000

For: METHOD OF FORMING A GATE  
ELECTRODE ON A  
SEMICONDUCTOR DEVICE AND A  
DEVICE INCORPORATING SAME

**RESPONSE TO NOTICE TO FILE CORRECTED APPLICATION PAPERS,**  
**DATED FEBRUARY 27, 2003**

Assistant Commissioner for Patents  
Washington, D.C. 20231

CERTIFICATE OF MAILING

I hereby certify that this paper or fee is being deposited with the United States Postal Service with sufficient postage as "FIRST CLASS MAIL" addressed to: Assistant Commissioner for Patents, Washington, DC 20231, on this the 10<sup>th</sup> day of March, 2003.

*Kathryn Janas*  
Signature

Sir:

This paper is submitted in response to the Notice to File Corrected Application Papers dated February 27, 2003, for which the one-month date for response is March 27, 2003.

It is believed that no fee is due; however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be required for any reason relating to this document, the Assistant Commissioner is authorized to deduct said fees from Advanced Micro Devices, Inc. Deposit Account No. 01-0365/TT3634. In the event the monies in that account are insufficient, the Assistant Commissioner is authorized to withdraw funds from Williams, Morgan & Amerson, P.C. Deposit Account No. 50-0786/2000.032200.